

BRS L3 95 2 and (square adj wave) USPAT, US-PGPUB, EPO, JPO, 2004/09/02 17:51 0		Туре		Hits			
DERWENT 17:51	•	BRS	L2	209	and (DC or (direct adj current)) and conver\$4 and (power near3	2004/09/02 17:51	0
DERWENT 16:30		BRS	L3	95	2 and (square adj wave)	(0
DERWENT 16:31	3	BRS	IA	65	3 and impedance		0
DERWENT 16:32 DERWENT DERWENT 16:32	4	BRS	L6	4	5 and microprocessor	1	0
DERWENT 17:52	5	BRS	L.7	3	6 and program\$5		0
and (DC or (direct adj current)) and conver\$4 and (power near3 DERWENT 17:51 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:52 17:53 17:53 17:53 17:53 17:53 17:53 17:53 17:53 17:53 17:53 17:56	6	BRS	L5	65			0
DERWENT 17:52 17:53 17:53 17:53 17:54 17:54 17:54 17:55 17:56	7	BRS	L11	366	and (DC or (direct adj current)) and conver\$4 and (power near3	,	0
DERWENT 17:52	3	BRS	L12	128	11 and (square adj wave)		0
DERWENT 17:53 11 BRS L16 3 14 and (external adj source) USPAT, US-PGPUB; EPO; JPO; 2004/09/02 17:53 12 BRS L15 16 14 and battery USPAT, US-PGPUB; EPO; JPO; 2004/09/02 DERWENT 17:56	3.	BRS	L13	90	12 and resonan\$3 and inductor and capacitor		0
11 BRS L16 3 14 and (external adj source) USPAT; US-PGPUB; EPO; JPO; 2004/09/02 0 DERWENT 17:53 0 0 12 BRS L15 16 14 and battery USPAT; US-PGPUB; EPO; JPO; 2004/09/02 DERWENT 17:56	All Control	BRS	L14	90			0
DERWENT 17:56	11	BRS	L16	3	14 and (external adj source)		0
		BRS	L15	16		1	0